

WCED Program

8:30-9:30AM Session I

- Effects of Pipeline Complexity on SMT/CMP Power-Performance Efficiency
B. Lee and D. Brooks (Harvard University)
- Early Performance Prediction
P. Kudva, B. Curran, S.K. Karandikar, M. Mayo, S. Carey (IBM), and S.S. Sapatnekar (University of Minnesota)
- Wire Management for Coherence Traffic in Chip Multiprocessors
L. Cheng, N. Muralimanohar, K. Ramani, R. Balasubramonian, and J. Carter (University of Utah)

9:30-9:50AM Break

9:50-10:30AM Session II

- Reducing the Power and Complexity of Path-Based Neural Branch Prediction
G.H. Loh (Georgia Institute of Technology) and D.A. Jiménez (Rutgers University)
- A Break-Even Formulation For Evaluating Branch Predictor Energy Efficiency
M. Co, D.A.B Weikle, and K. Skadron (University of Virginia)

10:30-11:00AM Break

11:00-11:40AM Session III

- Heuristics for Complexity-Effective Verification of a Cache Coherence Protocol Implementation
D. Abts (Cray), Y. Chen, and D.J. Lilja (University of Minnesota)
- The Design Complexity of Program Undo Support in a General-Purpose Processor
R. Teodorescu and J. Torrellas (University of Illinois at Urbana-Champaign)

11:40AM-12:30PM Metrics discussion led by J. Torrellas (University of Illinois at Urbana-Champaign)