

## Initial Results of Prototyping a 3-D Integrated Intra-Chip Free-Space Optical Interconnect

Berkehan Ciftcioglu, Rebecca Berman, Jian Zhang, Zach Darling, **Alok Garg**, Jianyun Hu, Manish Jain, Peng Liu, Ioannis Savidis, Shang Wang, Jing Xue, Eby Friedman, Michael Huang, Duncan Moore, Gary Wicks, and Hui Wu

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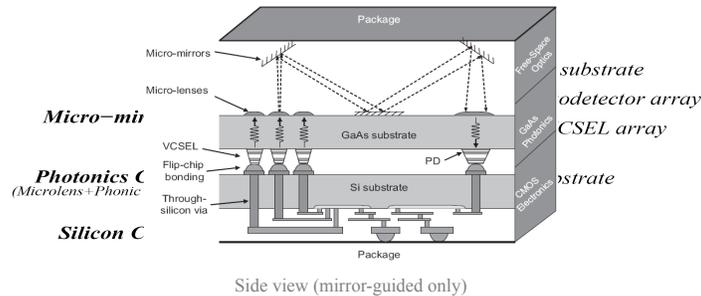
## Challenges for On-chip Optical Interconnect

- Signaling chain:
  - Efficient Si E/O modulators challenging
    - Inherently poor non-linear optoelectronic properties of Si
    - Resonator designs also non-ideal: e.g., e-beam lithography, temperature stability, insertion loss
  - Off-chip laser (expensive, impractical to power gate)
- Propagation medium:
  - In-plane waveguides add to the challenge and loss
    - Floor-planning, losses due to crossing, turning, and distance
  - Bandwidth density challenge
    - Density of in-plane wave guide limited
    - WDM: more stringent spectral requirements for devices and higher insertion losses, more expensive laser sources

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## Free-Space Optical Interconnect: an Alternative



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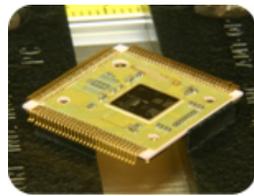
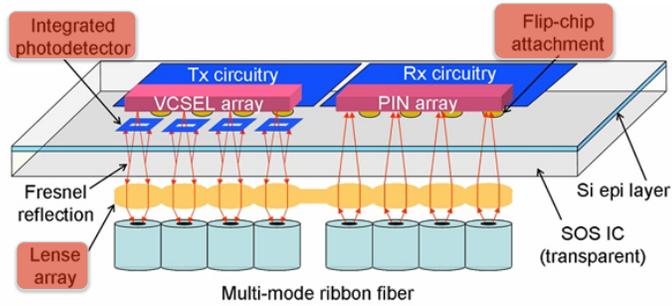
## Advantages of Free-Space Optical Interconnect

- Signaling: mostly current (commercially available) technology
  - ✓ Large VCSEL arrays, high-density (movable) micro mirrors, high-speed modulators and PDs
  - ✓ Integrated VCSELs (Vertical Cavity Surface Emitting Laser) avoids the need for external laser and optical power distribution
    - Disparate technology (e.g., GaAs)
- Propagation medium
  - ✓ Free-space: low propagation delay, low loss and low dispersion
    - Hindering heat dissipation
- Networking
  - ✓ Direct communication: relay-free, low overhead, no network deadlock or the necessity to prevent it
  - ✓ Route virtual wires instead of packets
- New opportunities for system designers
  - ✓ Optimize communication (e.g. cache coherence protocol)

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## Commercially Available Tech.

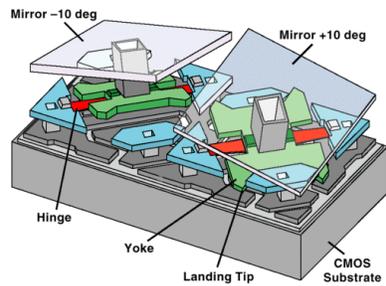
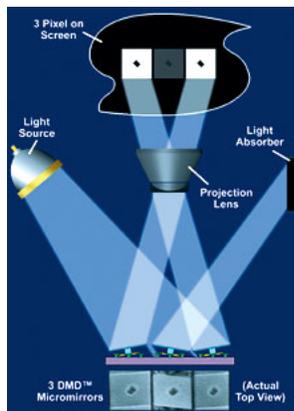


- IC with
- VCSELs
  - PINs
  - Photodetectors
  - Micro-lenses

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## Digital Micromirror Devices by TI



Micro-Optical Electro-Mechanical Device - MOEMS



**900,000 microscopic mirrors**

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## Challenges: Cooling Technologies

**Cooling using peltier effect - Nextreme**

Liquid cooling

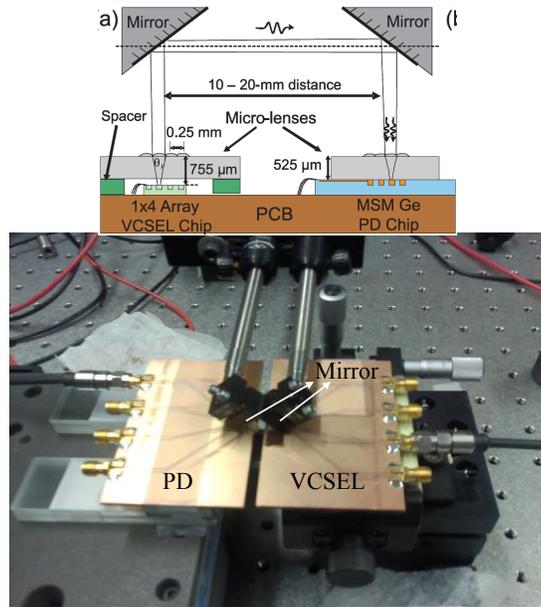
Graphene

Nextreme Thermal Bump

Flip Chip Integration

## Prototype and Measurement Results

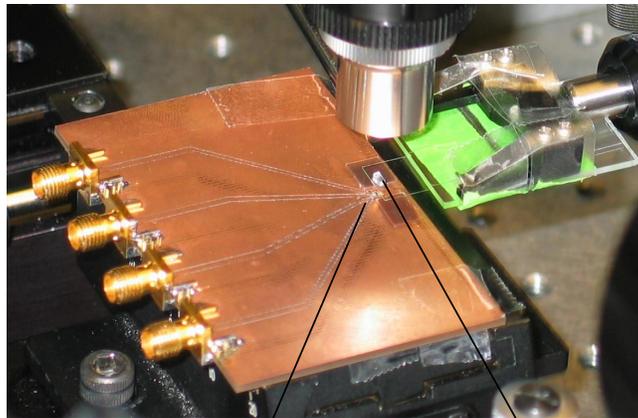
## Link Demo on Board Level



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## VCSEL and Microlense



*Commercial VCSELs*

Divergence: 30°  
Speed: 10 Gb/s

*Microlenses*

Radius of curvature: 1.22 mm  
Focal point: 730 μm

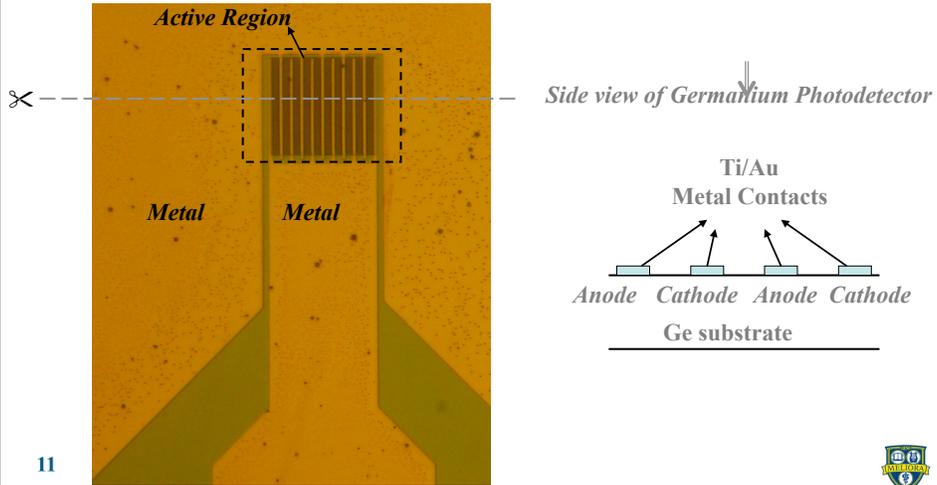
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## Germanium Photodetector

To appear in *Photonics Technology Letters*

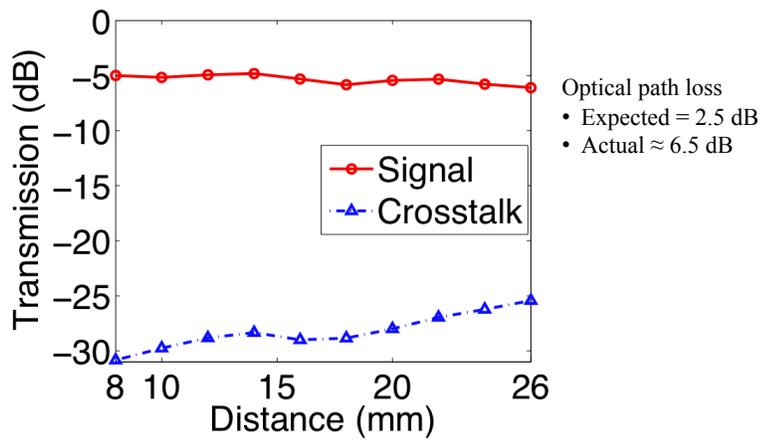
Bandwidth: 13 GHz



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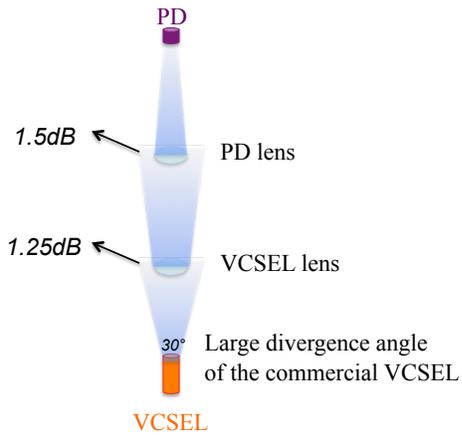
## Transmission and Crosstalk



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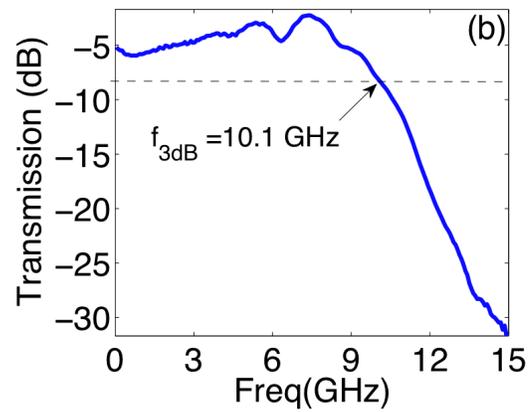
## Transmission Losses Due to Beam Clipping



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## Small Signal Bandwidth at 1cm



Note: Small signal bandwidth does not change with distance

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## Summary

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- Fully-distributed free-space optical interconnect provides an alternative
- Technology readiness
  - Entire signaling chain is commercially available in large scale
  - 3D integration of disparate technologies common in small scale SoCs
  - Thermal issues may be avoided by piggybacking on other developments
- Initial prototyping results encouraging

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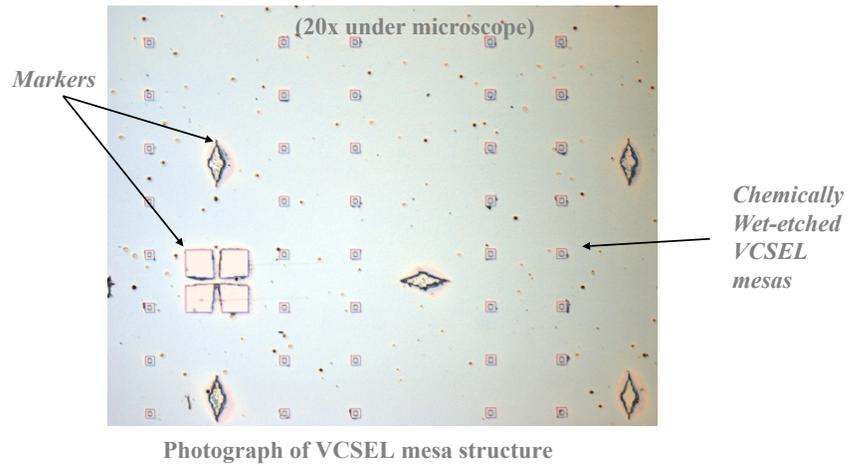
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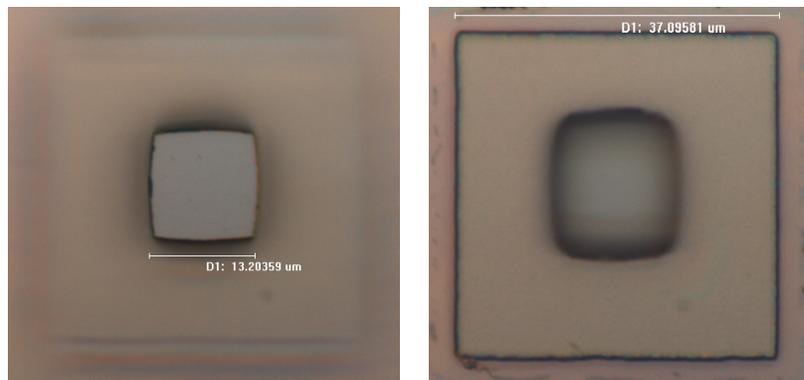
## Prototype Custom-Made VCSEL Arrays



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## Single VCSEL Structure (Under Microscope)



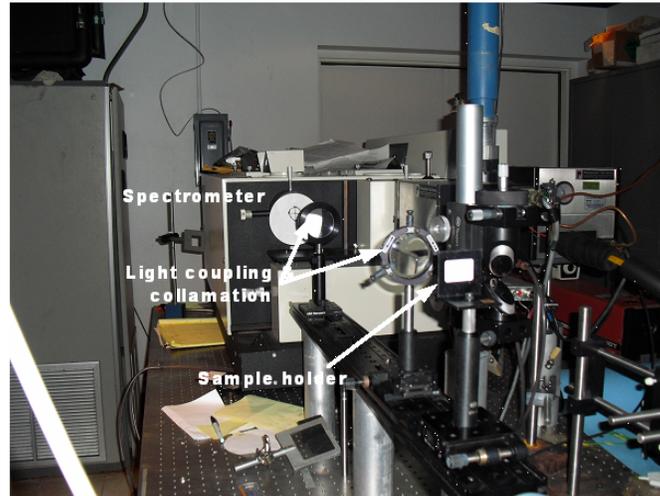
a) Top view of the etched mirrors

b) The p-contact region of the VCSEL, located below the mirrors shown in a)

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## Spectrometer Setup

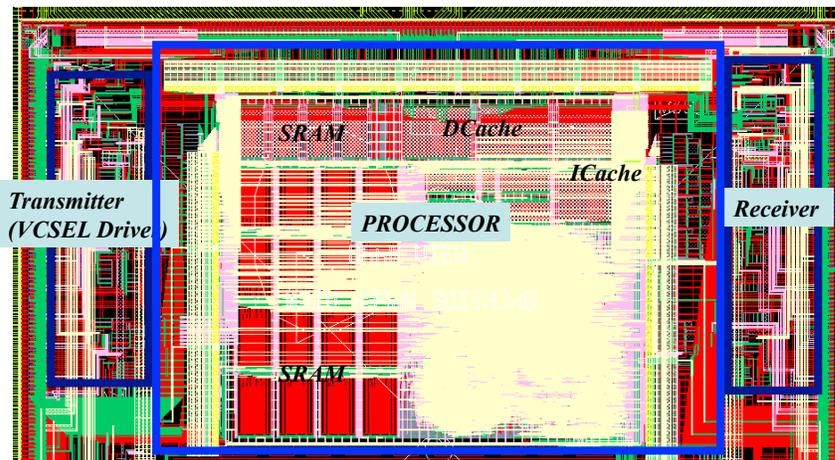


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## 3D Test Chip for System-Level Demo

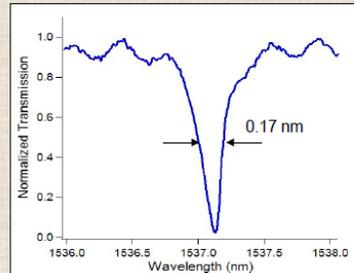
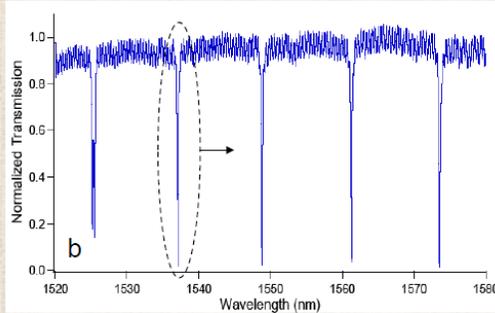
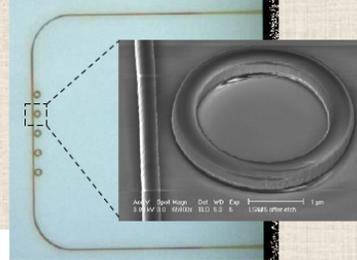


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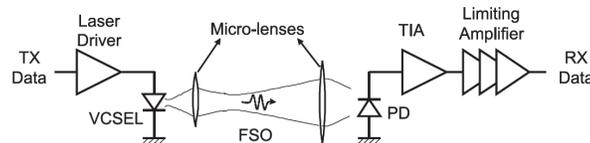
## High-Q 1.5- $\mu\text{m}$ -radius microring resonator

- Resonances from five cascaded microring resonators with slightly different radii  $\sim 1.5 \mu\text{m}$
- High Q of 9,000 (BW  $\sim 20 \text{ GHz}$ ) and high extinction ratio of 16 dB.



Q. Xu et al., *Opt. Express* **16**, 4309 (2008)

## Efficient Optical Links



Free-Space Optics	
Transmission distance	2 cm
Optical wavelength	980 nm
Micro-lens aperture	90 $\mu\text{m}$ @ transmitter, 190 $\mu\text{m}$ @ receiver
Optical path loss	2.6 dB
Transmitter & Receiver	
VCSEL	aperture=5 $\mu\text{m}$ , parasitic=235 $\Omega$ // 90 fF threshold=0.14 mA, extinction ratio=11:1 bandwidth=43 GHz
Laser driver	responsivity=0.5 A/W, capacitance=100 fF
PD	bandwidth=36 GHz, gain=15000 V/A
TIA & Limiting amp	
Link	
Data rate	40 Gbps
Signal-to-noise ratio	7.5 dB
Bit-error-rate (BER)	$10^{-10}$
Cycle-to-cycle jitter	1.7 ps
Power Consumption	
Transmitter (active)	0.96 mW for VCSEL (0.48 mA@2V) 6.3 mW for laser driver
Transmitter (standby)	0.43 mW
Receiver	4.2 mW

## Related Work

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- Buffer-less optical packet-switched network, Schacham and Bergman, *IEEE Micro* 2007
- Circuit-switched optical network, Schacham et al. NOC'07
- Bus or ring-based shared-medium optical interconnect
  - Ha and Pinkston *JPDC* 1997
  - HP Corona (Beausoleil *LEOS* 2008, Vantrease et al. ISCA'08)
  - Kirman et al. MICRO'06
- Free-space optics
  - Miller, *J. Sel. Top. in Quantum Elec.* 2007
  - Krishnamoorthy and Miller, *JPDC* 1997
  - Marchand et al. *JPDC* 1997
  - Walker et al. *Applied Optics* 1998