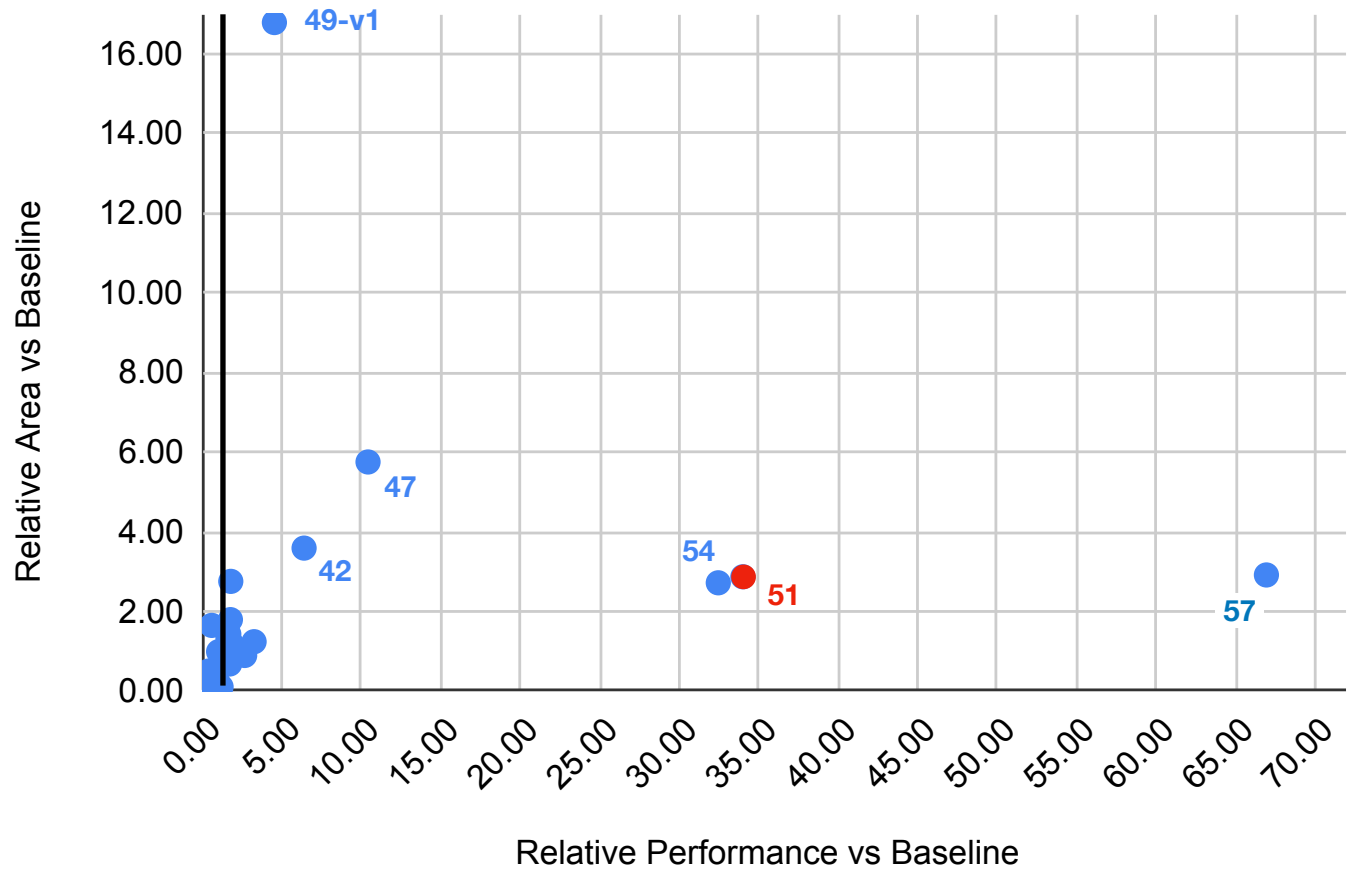
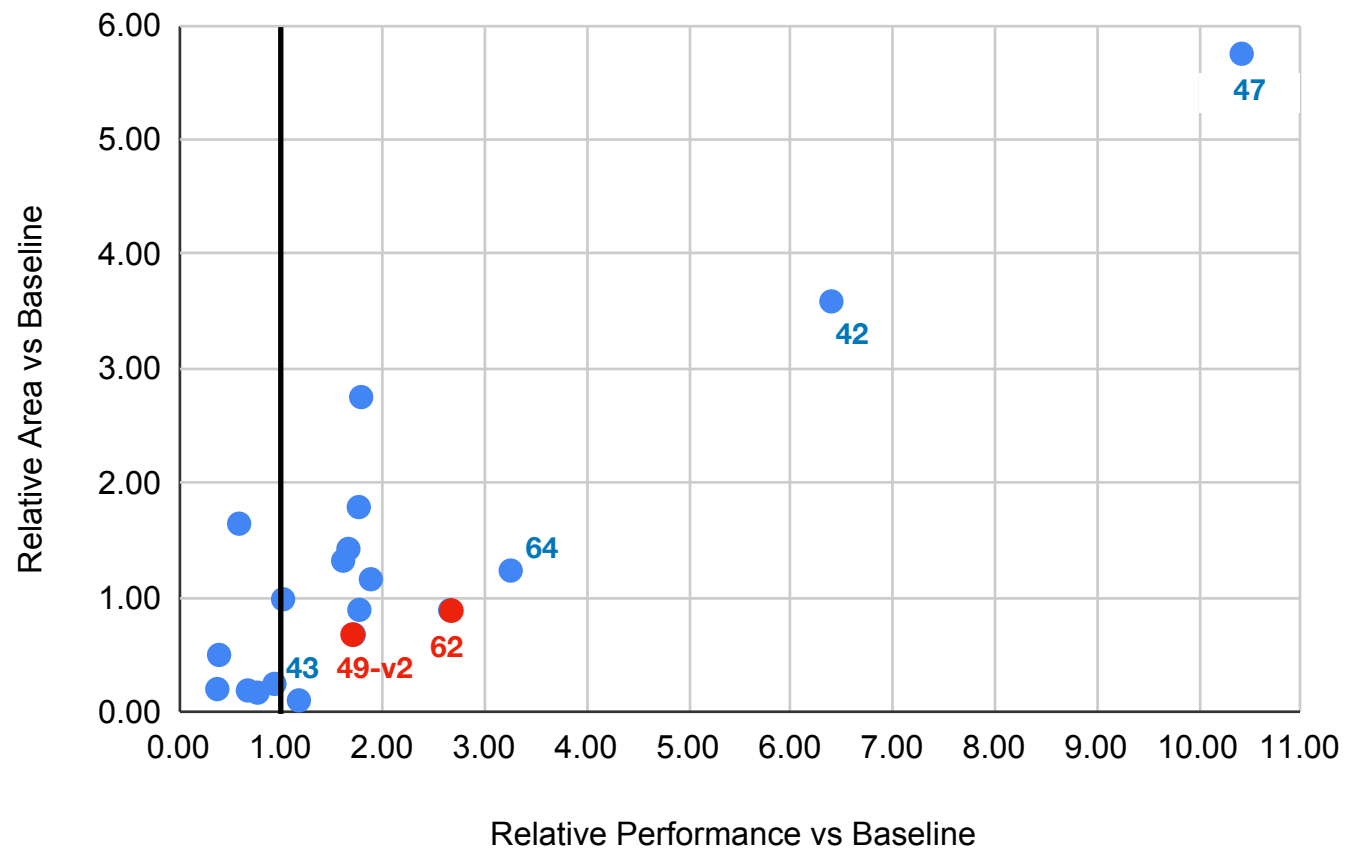


Rand ID	eligible	comment	area	clock	slack	Results for 100 Element Array					Area Over head	sz100 pareto
						sz100a	sz100b	sz100c	avg sz100	Norm Perf		
base			17025	3.0	0.829	15162	15546	14275	14994	1.00		
bsort			3489	3.0	0.829	40017	40017	40017	40017	0.37	0.20	
57-v1	yes		49594	3.0	0.397	224	224	224	224	66.94	2.91	Y
51-v1	yes	sz100+sz1K pareto	48827	3.0	0.136	441	441	441	441	34.00	2.87	Y
54-v1	yes		46248	3.0	0.498	462	462	462	462	32.46	2.72	Y
47-v1	yes		97856	3.0	0.037	1439	1439	1439	1439	10.42	5.75	
42-v1	yes		61070	3.0	0.101	2344	2344	2344	2344	6.40	3.59	
49-v1	yes		285549	3.0	0.041	3326	3288	3316	3310	4.53	16.77	
64-v1	yes		21091	3.0	0.051	4611	4611	4611	4611	3.25	1.24	Y
62-v1	yes	sz100+sz1K pareto	15211	3.0	0.476	5641	5641	5641	5641	2.66	0.89	Y
56-v1	yes		19801	3.0	0.838	8094	8107	7720	7974	1.88	1.16	
60-v1	yes		46861	3.0	0.195	8554	8486	8152	8397	1.79	2.75	
44-v1	yes		15254	3.0	1.329	8507	8486	8455	8483	1.77	0.90	
41-v1	yes		30522	3.0	0.046	8535	8513	8481	8510	1.76	1.79	
49-v2	yes	sz100+sz1K pareto	11575	3.0	1.587	8783	8713	8749	8748	1.71	0.68	Y
58-v1	yes		24304	3.0	1.062	9049	9004	9040	9031	1.66	1.43	
48-v1	yes		22561	3.0	0.446	9336	9225	9400	9320	1.61	1.33	
43-v1	yes		1806	3.0	2.249	12390	13522	12377	12763	1.17	0.11	Y
40-v1	yes		16830	3.0	0.478	14732	14662	14708	14701	1.02	0.99	
63-v1	no	no speedup	4259	3.0	0.453	16021	16039	16045	16035	0.94	0.25	
61-v1	no	no speedup	2957	3.0	0.755	20581	20101	17759	19480	0.77	0.17	
45-v1	no	no speedup	3284	3.0	0.608	23504	22943	20246	22231	0.67	0.19	
55-v1	no	no speedup	28056	3.0	0.438	26839	25592	23962	25464	0.59	1.65	
50-v1	no	no speedup	8562	3.0	0.442	38230	38230	38230	38230	0.39	0.50	
59-v1	no	failed RTL+GL sim	n/a	n/a	n/a	423	423	423	423	35.45	n/a	
53-v1	no	failed RTL+GL sim	48187	3.0	0.498	timeout	timeout	timeout	timeout	timeout	2.83	
			area	clock	slack	Results for 1000 Element Array					Area Over head	sz1K pareto
						sz1Ka	sz1Kb	sz1Kc	avg sz1K	Norm Perf		
base			17025	3.0	0.829	200771	202684	193315	198923	1.00		
bsort			3489	3.0	0.829	4000017	4000017	4000017	4000017	0.05	0.20	
47-v1	yes		97856	3.0	0.037	13735	13735	13735	13735	14.48	5.75	Y
51-v1	yes	sz100 & sz1K pareto	48827	3.0	0.136	44585	44587	44593	44588	4.46	2.87	Y
62-v1	yes	sz100 & sz1K pareto	15211	3.0	0.476	56041	56041	56041	56041	3.55	0.89	Y
64-v1	yes		21091	3.0	0.051	75401	75401	75401	75401	2.64	1.24	
57-v1	yes		49594	3.0	0.397	79090	79114	79070	79091	2.52	2.91	
49-v1	yes		285549	3.0	0.041	109579	109629	109569	109592	1.82	16.77	
56-v1	yes		19801	3.0	0.838	156673	157036	156162	156624	1.27	1.16	
44-v1	yes		15254	3.0	1.329	161544	161614	161389	161516	1.23	0.90	
41-v1	yes		30522	3.0	0.046	162529	162637	162425	162530	1.22	1.79	
54-v1	yes		46248	3.0	0.498	163298	163268	163298	163288	1.22	2.72	
49-v2	yes	sz100 & sz1K pareto	11575	3.0	1.587	169839	169937	170043	169940	1.17	0.68	Y
42-v1	yes		61070	3.0	0.101	200779	202692	193323	198931	1.00	3.59	
60-v1	yes		46861	3.0	0.195	200779	202692	193323	198931	1.00	2.75	
43-v1	no	no speedup	1806	3.0	2.249	203354	209706	231886	214982	0.93	0.11	
40-v1	no	no speedup	16830	3.0	0.478	250812	250777	250451	250680	0.79	0.99	
48-v1	no	no speedup	22561	3.0	0.446	309426	309084	309408	309306	0.64	1.33	
58-v1	no	no speedup	24304	3.0	1.062	534504	534099	534366	534323	0.37	1.43	
63-v1	no	no speedup	4259	3.0	0.453	1510459	1510465	1510483	1510469	0.13	0.25	
55-v1	no	no speedup	28056	3.0	0.438	1627209	1583162	1588211	1599527	0.12	1.65	
61-v1	no	no speedup	2957	3.0	0.755	1756517	1706460	1711036	1724671	0.12	0.17	
45-v1	no	no speedup	3284	3.0	0.608	2011039	1953635	1959093	1974589	0.10	0.19	
50-v1	no	no speedup	8562	3.0	0.442	3832180	3832180	3832180	3832180	0.05	0.50	
59-v1	no	failed RTL+GL sim	n/a	n/a	n/a	timeout	timeout	timeout	timeout	timeout	n/a	
53-v1	no	failed RTL+GL sim	48187	3.0	0.498	timeout	timeout	timeout	timeout	timeout	2.83	

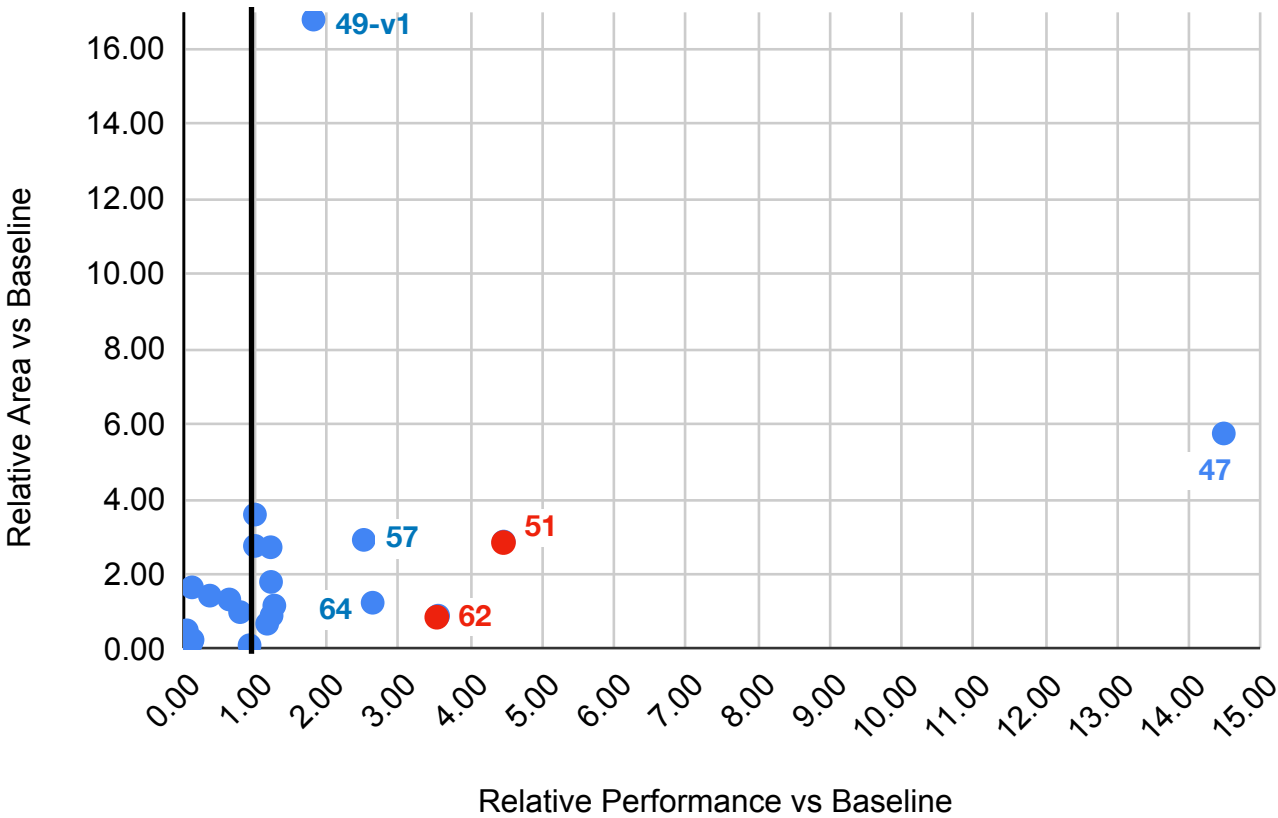
Results for 100 Element Array



Results for 100 Element Array



Results for 1000 Element Array



Results for 1000 Element Array

