

# ECE 4750 Computer Architecture, Fall 2023

## Next Steps

School of Electrical and Computer Engineering  
Cornell University

revision: 2023-08-21-00-58

### 1. Speak with instructor if you have a conflict with discussion section

Students are expected to make every effort to attend the discussion section. However, if you have a legitimate course conflict with the discussion section, please speak with the instructor. We don't want anyone not to be able to take ECE 4750 because of a conflict with the discussion section.

### 2. Verify you can view the Canvas course site and Ed discussion forum

We will be using Canvas for distributing course materials, collecting assignments, and distributing grades. Please ensure that if you are officially enrolled in the course, you can view this course in Canvas. We will use Ed for all announcements, online discussions, and most student/instructor communication. Students officially enrolled should already be automatically added to the Ed discussion forum for this course. Please use the link in Canvas to make sure you can view the Ed discussion forum.

### 3. Read the course syllabus

The course syllabus contains essential information about the course motivation, structure, procedures, and policies. It will be assumed that all students have read and understand all of the material in the course syllabus. We will not waste lecture time repeating what is in the syllabus, so it is difficult to underemphasize how important it is to read the entire syllabus!

### 4. Prepare for first quiz

There will be a short five-minute quiz at the very beginning of lecture on Wednesday, August 23rd on the three eras of computer architecture and on the ECE 4750 collaboration policy as described in the course syllabus. You should read the slides from the first lecture and the course syllabus to prepare.

### 5. Work through lab tutorials

We are preparing several tutorials covering remote access to the ecelinux servers, the Linux development environment, Git version control, and Verilog. The tutorials have critical information. Please work through these tutorials when they are posted on Canvas.

### 6. Read Chapter 1 of Hennessy and Patterson

The course textbook is "*Computer Architecture: A Quantitative Approach, 5th Edition*," by J. L. Hennessy and D. A. Patterson (Morgan Kaufmann, 2012). For your convenience, the first chapter is available on Canvas under readings.

## 7. Review background material as necessary

Students who are less confident of their mastery of the material covered in the pre-requisite courses should probably begin reviewing the material in “*Digital Design and Computer Architecture, 2nd Edition*,” by D. Harris and S. Harris (Morgan Kaufmann, 2012). If a student has less experience working with Verilog but still wants to use this language, then they are strongly encouraged to read Chapter 4 in Harris and Harris on digital design with Verilog and/or to review the optional text “*Verilog HDL: A Guide to Digital Design and Synthesis, 2nd edition*” by S. Palnitkar (Prentice Hall, 2003).

## 8. Begin selecting a group for the lab assignments

The lab assignments are specifically designed to be a reasonable amount of work for two students working together. Students can either form their own group or have the instructor form a group for them. If you would like the instructor to form a group for you, then please submit a request using the online form located here <http://www.cs1.cornell.edu/courses/ece4750/labgroup> by 9:30pm on Wednesday, August 31st. Groups will be announced the following day. Only submit a request if you are formally enrolled in the course. You should complete the tutorials *and* you should start working on the first lab assignment even before groups are assigned!

## 9. Attend discussion section on Friday in G01 Gates Hall

Students are expected to attend the weekly discussion section. This week will be reviewing the Linux development environment we will be using in the lab assignments. Even if you are experienced in Linux you should still come. We need to make sure you can access the ecelinux servers and the rest of the infrastructure we will be using in this course. The discussion section is on Friday from 2:30–3:20pm in G01 Gates Hall. **Please bring your laptop.**