

ECE 2300 Digital Logic and Computer Organization, Fall 2026

Lab 0: Worksheet

NAME: _____

NetID: _____

1.3. Multi-Input Circuit 1 Simulation

Fill in the following table using your simulation of multi-input circuit 1.

Va	Vb	Vy
0V	0V	
0V	3.3V	
3.3V	0V	
3.3V	3.3V	

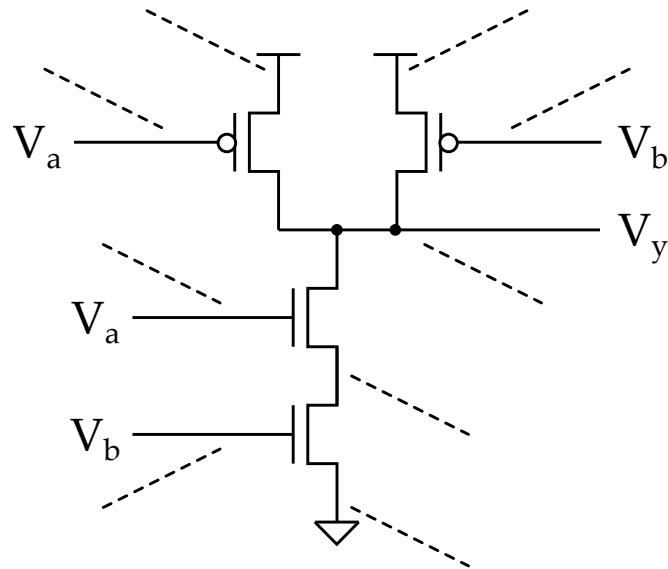
1.4. Multi-Input Circuit 2 Simulation

Fill in the following table using your simulation of multi-input circuit 2.

Va	Vb	Vy
0V	0V	
0V	3.3V	
3.3V	0V	
3.3V	3.3V	

3.1. Multi-Input Circuit 1 Prototype

Label each wire on the following transistor-level schematic with the corresponding color of the wire used in your actual breadboard prototype.



3.2. Multi-Input Circuit 2 Prototype

Label each wire on the following transistor-level schematic with the corresponding color of the wire used in your actual breadboard prototype.

