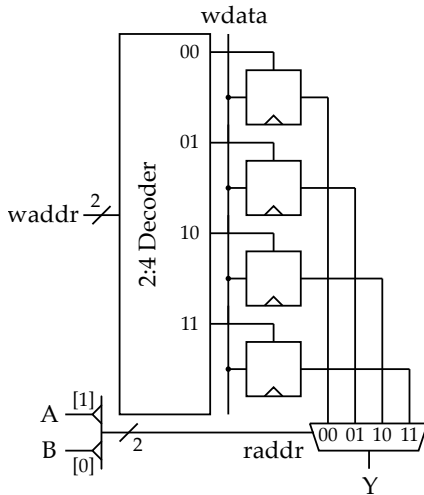
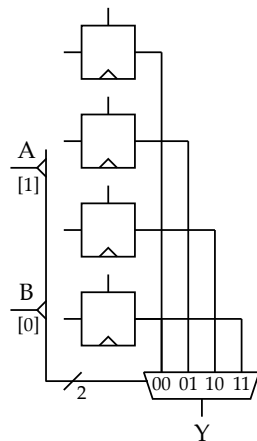
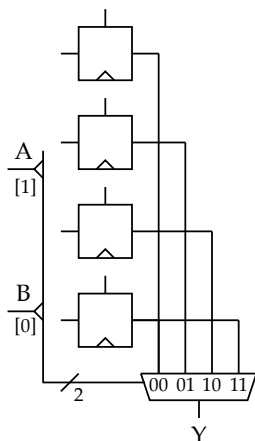
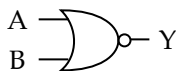
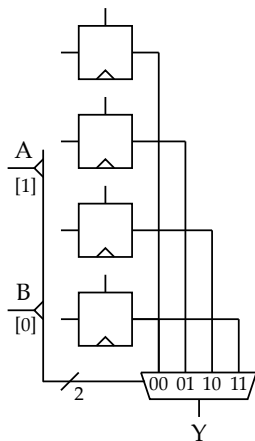
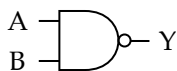
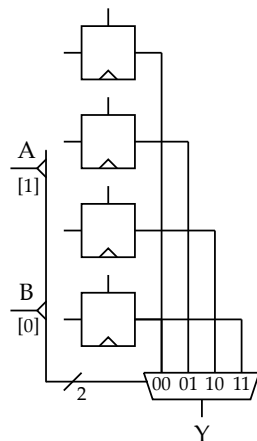
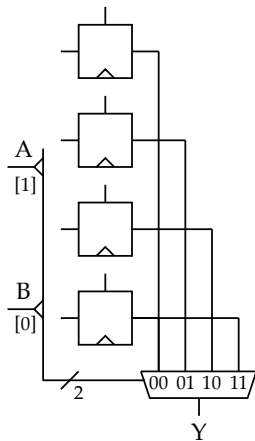
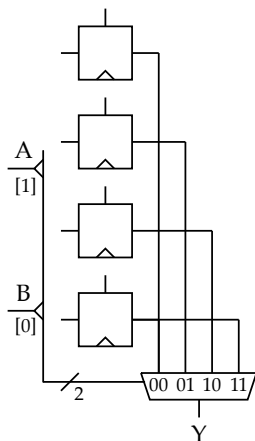
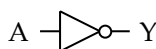


7. Programmable Logic

- Programmable logic uses sequential logic-gates to enable reconfiguring (“programming”) the behavior of hardware after a chip has been fabricated

7.1. Programmable Truth Tables





7.2. Field Programmable Gate Arrays

