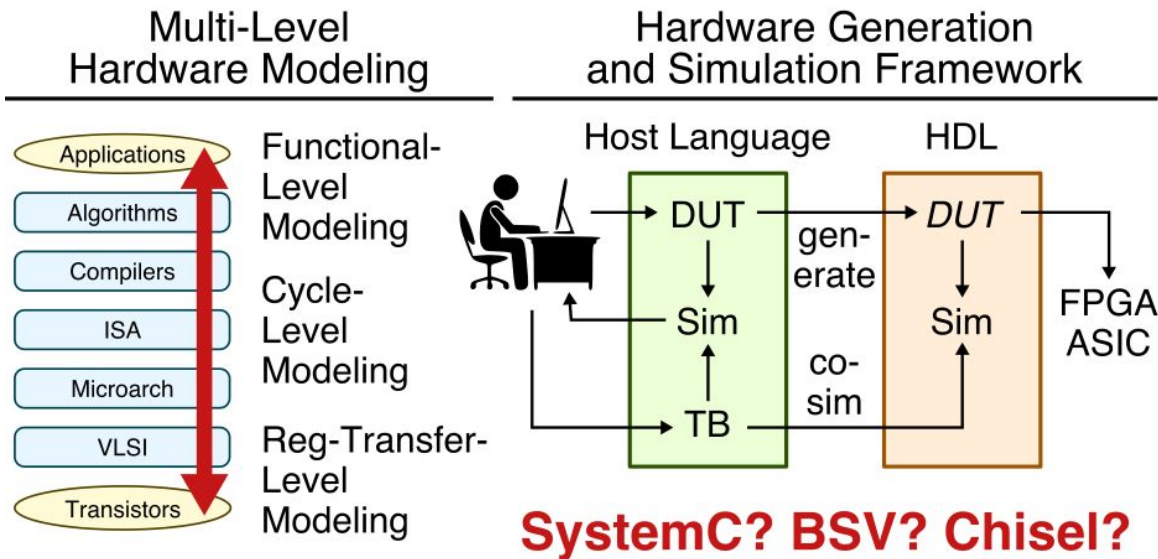


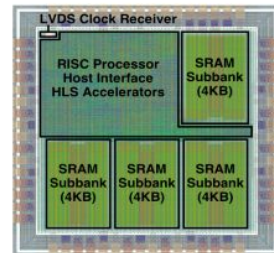
Christopher Batten, Cornell University

PyMTL: Using Python to Design, Model, Verify, & Tapeout Chips



PyMTL: A Python-based hardware generation & simulation framework for building SoCs which enables productive multi-level modeling and VLSI design

- Used by 300+ Cornell students
- Used in several architecture papers from Cornell groups
- Used in three ASIC tapeouts in GF 130nm, TSMC 28nm, TSMC 16nm



- Derek Lockhart et al., "PyMTL: A Unified Framework for Vertically Integrated Computer Architecture Research," MICRO 2014. <https://github.com/cornell-brg/pymtl>
- Shunning Jiang et al., "Mamba: Closing the Performance Gap in Productive Hardware Development Frameworks," DAC 2018. <https://github.com/cornell-brg/mamba-dac2018>