Christopher Batten, Cornell University

PyMTL: Using Python to Design, Model, Verify, & Tapeout Chips

PyMTL: A Python-based hardware generation & simulation framework for building SoCs which enables productive multi-level modeling and VLSI design

- Used by 300+ Cornell students
- Used in several architecture papers from Cornell groups
- Used in three ASIC tapeouts in GF 130nm, TSMC 28nm, TSMC 16nm

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