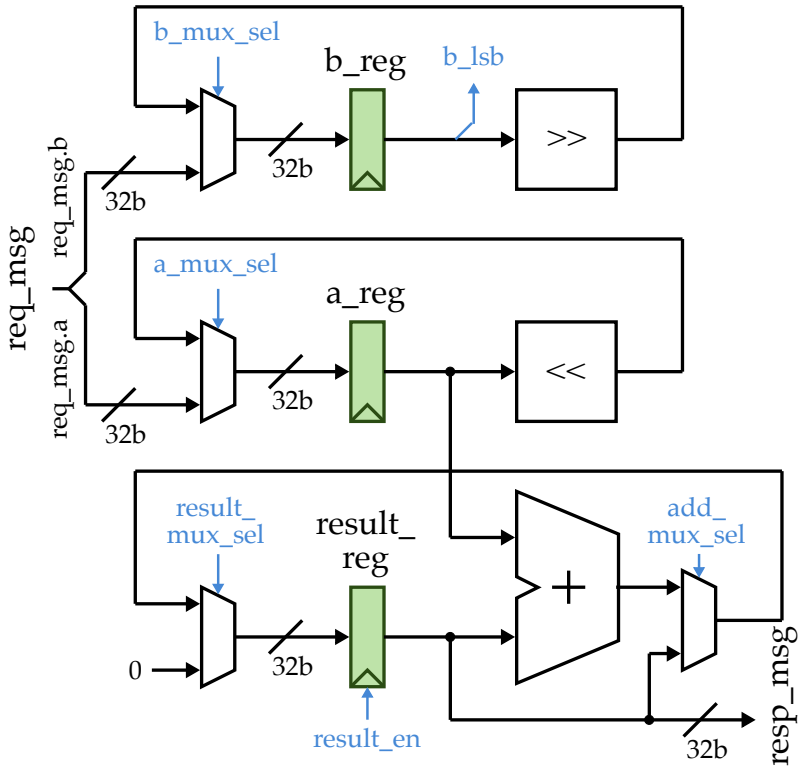
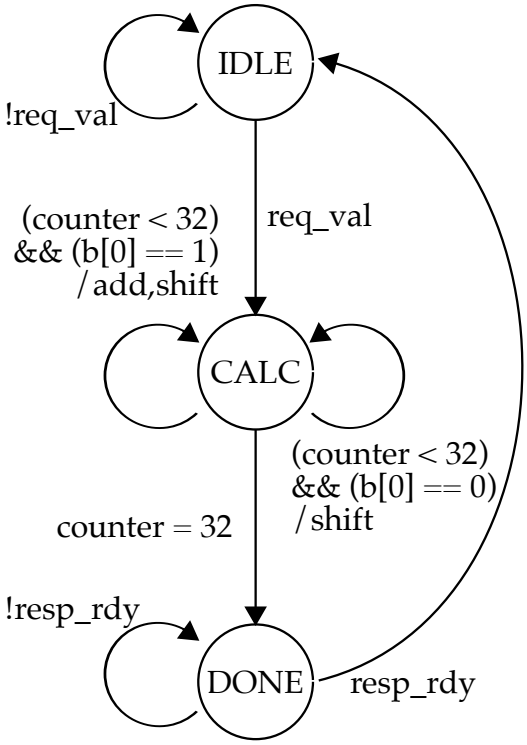
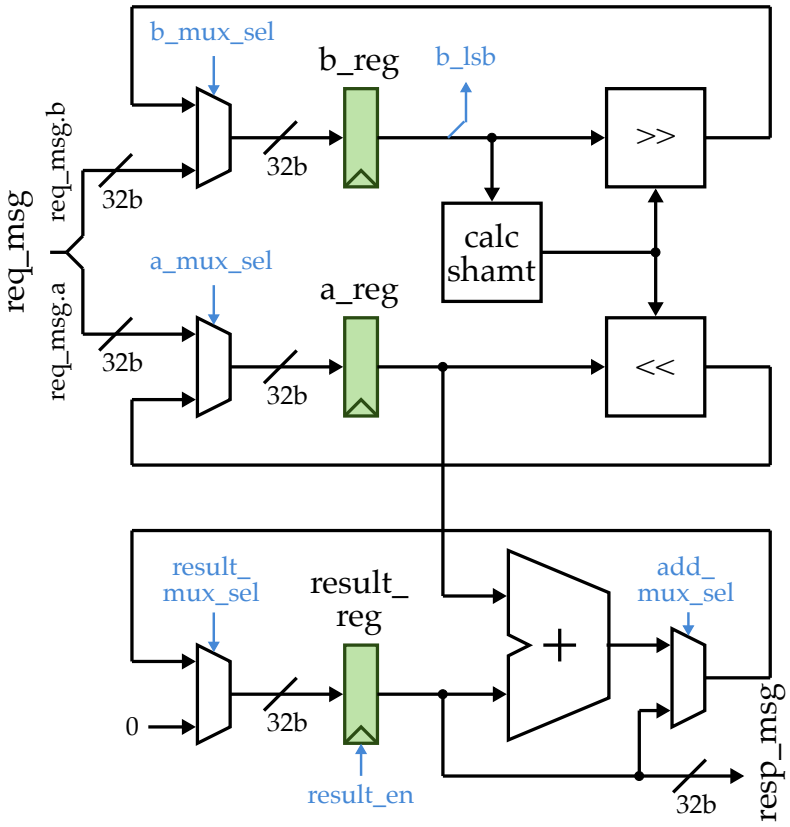
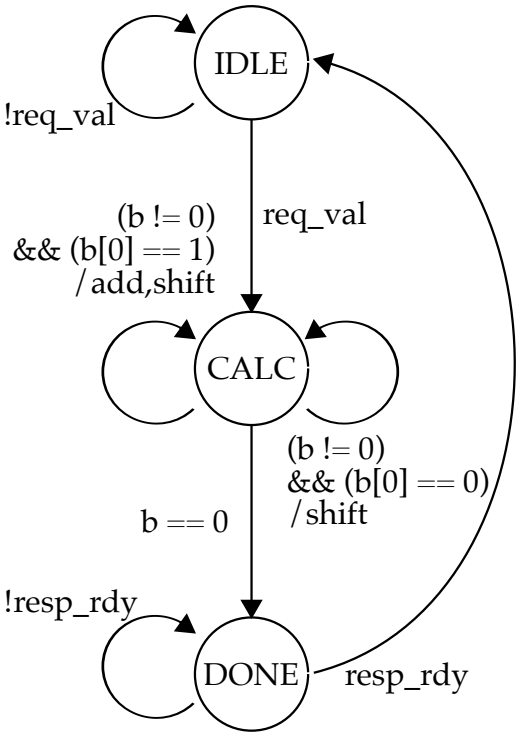
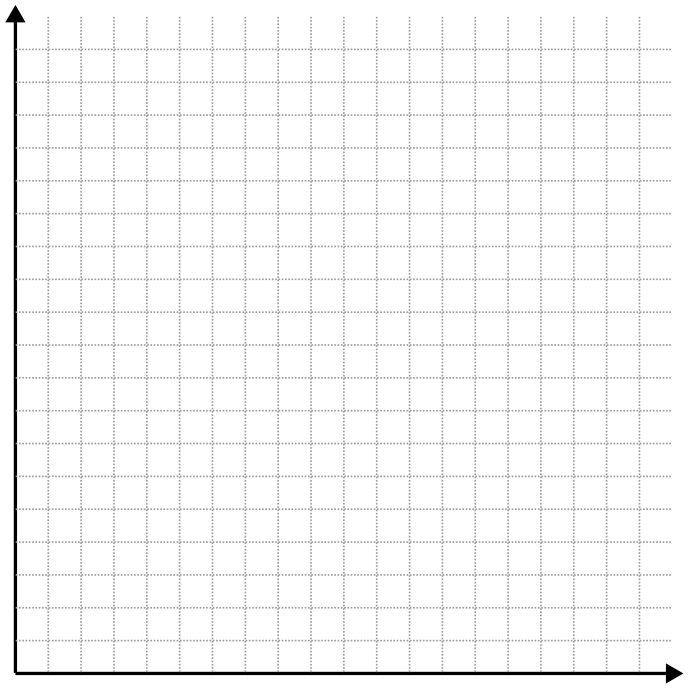
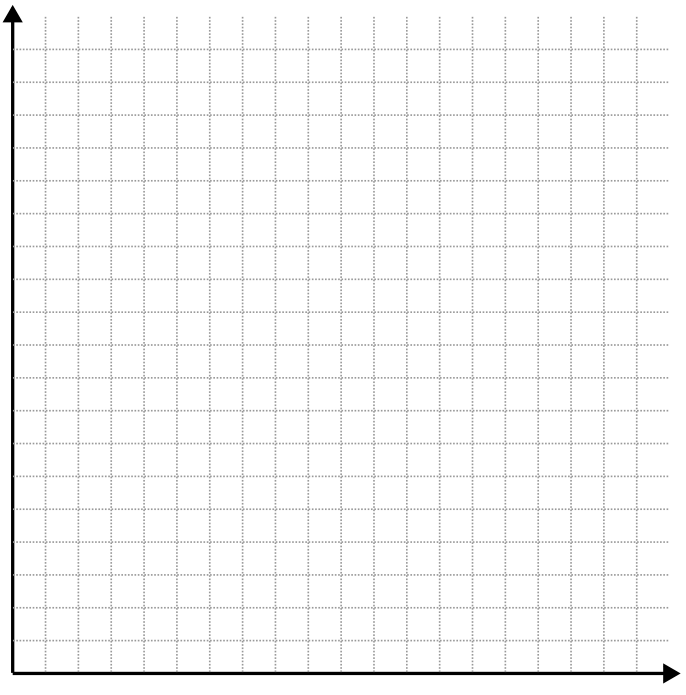
ECE 4750 Computer Architecture
Section 7: Open-Source Hardware









	Baseline Design	Alternative Design
Total Area (um^2)		
- Counter		
- Left Shifter		
- Right Shifter		
- ResetReg		
- EnReg		
- Mux2		
- Reg		
- SimpleAdder		
- ZeroComparator		
- CalcShamt		
Cycles/Transaction		
Cycle Time (ns)		
Time/Transaction (ns)		
Power (mW)		
Energy/Transaction (pJ)