

Component	Delay Equation	Delay ( $\tau$ )
addr_reg_M0	1	1
tag_decoder	$3 + 2 \cdot 3$	9
tag_mem	$10 + \lceil (8 + 26) / 16 \rceil$	13
tag_cmp	$3 + 2 \lceil \log_2(25) \rceil$	13
tag_and	1	1
hit_mux	$3 \lceil \log_2(2) \rceil + \lceil 1/8 \rceil$	4
hit_reg_Mx	1	1
<b>Total</b>		<b>42</b>
addr_reg_M0	1	1
addr_mux	$3 \lceil \log_2(2) \rceil + \lceil 5/8 \rceil$	4
data_decoder	$3 + 2 \cdot 3$	9
data_mem	$10 + \lceil (8 + 128) / 16 \rceil$	19
rdata_mux	$3 \lceil \log_2(4) \rceil + \lceil 32/8 \rceil$	10
rdata_reg_Mx	1	1
<b>Total</b>		<b>44</b>