lw	F	D	I	B0	B1	W	C							1	2	3	4	5								
lw	F	D	i	I	B0	B1	W	C																		
+		F	D	i	...	I	A	W	C																	
sw		F	D	i	...	...	I	B0	B1	W	C															
+i			F	D	I	A	W	r	...	...	C															2. ooo execution
+i			F	D	I	B0	B1	W	r	...	...	C														
+i				F	D	I	B0	B1	W	r	...	C														
+i				F	D	i	I	A	W	r	...	...	C													
bne					F	D	i	I	A	W	r	...	C													
opA					F	/																				
lw						F	D	I	B0	B1	W	r	...	C												5. branch prediction
lw						F	D	i	I	B0	B1	W	r	C												6. speculative execution
+							F	D	i	...	I	A	W	r	C											
sw							F	D	i	...	...	I	B0	B1	W	C										
+i								F	D	I	A	W	r	...	...	C										3. reg rename (earlier write
+i								F	D	I	B0	B1	W	r	...	...	C									to destination still in flight)
+i									F	D	I	B0	B1	W	r	...	C									
+i									F	D	i	I	A	W	r	...	...	C								
bne										F	D	i	I	A	W	r	...	C								
opA										F	/															
lw											F	D	I	B0	B1	W	r	...	C							4. mem disambig
lw											F	D	i	I	B0	B1	W	r	C							
+												F	D	i	...	I	A	W	r	C						
sw												F	D	i	...	...	I	B0	B1	W	C					
+i													F	D	I	A	W	r	...	...	C					
+i													F	D	I	B0	B1	W	r	...	...	C				
+i														F	D	I	B0	B1	W	r	...	C				
+i														F	D	i	I	A	W	r	...	...	C			
bne															F	D	i	I	A	W	r	...	C			
opA															F	/										
lw																F	D	I	B0	B1	W	r	...	C		
lw																F	D	i	I	B0	B1	W	r	C		